

**IN THE CLAIMS:**

Claims 15 - 17 are amended. In the following the total set of claims 1 to 27 is listed again :

1.-14. (CANCELLED)

15. (AMENDED) A Pockels cell driver circuit, comprising

- a first circuit node (SK1) to be connected with a first connector of the Pockels cell (CP) and a second circuit node (SK2) to be connected with a second connector of the Pockels cell (CP), wherein
- the first circuit node (SK1) is connected with a first potential via a first switch (S1), and
- the second circuit node (SK2) is connected with the first potential via a second switch (S2), wherein
- both said circuit nodes (SK1, SK2) are connected with a second potential (HV) via a recharging resistor (R1, R2), respectively, and
- only one (SK2) of the said circuit nodes (SK1, SK2) or both said circuit nodes (SK1, SK2) are connected with the second potential (HV) via a further switch (S2B), respectively, wherein
- said Pockels cell driver circuit is further arranged to switch the voltage at the Pockels cell (PC) on and off by switching each one of the first switch (S1), the second switch (S2) and the further switch (S2B) on and off with one and the same repetition rate.

16. (AMENDED) A Pockels cell driver circuit, comprising

- a first circuit node (SK1) to be connected with a first connector of the Pockels cell (CP) and a second circuit node (SK2) to be connected with a second connector of the Pockels cell (CP), wherein
- the first circuit node (SK1) is connected with a first potential via a first switch (S1), and

- the second circuit node (SK2) is connected with the first potential via a second switch (S2), wherein
- one of the said circuit nodes (SK1, SK2) is connected with a second potential (HV) via a recharging resistor, and the other one of the said circuit nodes (SK1, SK2) is connected with the second potential (HV) via a further switch, wherein
- said Pockels cell driver circuit is further arranged to switch the voltage at the Pockels cell (PC) on and off by switching each one of the first switch (S1), the second switch (S2) and the further switch on and off with one and the same repetition rate.

17. (AMENDED) A Pockels cell driver circuit, comprising

- a first circuit node (SK1) to be connected with a first connector of the Pockels cell (CP) and a second circuit node (SK2) to be connected with a second connector of the Pockels cell (CP), wherein
- the first circuit node (SK1) is connected with a first potential via a first switch (S1), and
- the second circuit node (SK2) is connected with the first potential via a second switch (S2), wherein
- ~~both said circuit nodes (SK1, SK2) are connected with a second potential (HV) via a switch (S1B, S2B), respectively.~~
- said first circuit node (SK1) is connected with a second potential (HV) via a third switch (S1B) and said second circuit node (SK2) is connected with the second potential (HV) via a forth switch (S2B), wherein
- said Pockels cell driver circuit is further arranged to switch the voltage at the Pockels cell (PC) on and off by switching each one of the first switch (S1), the second switch (S2), the third switch (S1B) and the forth switch (S2B) on and off with one and the same repetition rate.

18. (PREVIOUSLY PRESENTED) The Pockels cell driver circuit according to one of the claims 15 to 17, wherein

- low voltage control signals individually control each of the three or four switches (S1A, S1B, S2A, S2B) of the circuit.

19. (PREVIOUSLY PRESENTED) The Pockels cell driver circuit according to one of the claims 15 to 17, wherein

- only two control signals (ON, OFF) control all three or four switches (S1A, S1B, S2A, S2B) such that one of the control signals (ON) induces voltage to be applied to the Pockels cell and the other control signal (OFF) induces the removal of voltage from the Pockels cell.

20. (PREVIOUSLY PRESENTED) A System comprising a Pockels cell and a Pockels cell driver circuit according to one of the claims 15 to 17 connected thereto.

21. (PREVIOUSLY PRESENTED) A pulse laser system and a system according to claim 20 for the optical switching of laser pulses.

22. (PREVIOUSLY PRESENTED) The pulse laser system according to claim 21, wherein the pulse laser system comprises a pulsed laser source (1) having a laser resonator, wherein the Pockels cell (2) is arranged internally or externally to the laser resonator.

23. (PREVIOUSLY PRESENTED) The pulsed laser system according to one of claims 21 or 22, wherein the pulsed laser system comprises a pulsed laser source (1) and an optical amplifier (4).

24. (PREVIOUSLY PRESENTED) The pulsed laser system according to claim 23, wherein the system according to claim 20 is contained within the optical amplifier (4).

25. (PREVIOUSLY PRESENTED) The pulsed laser system according to one of claims 22 or 23, wherein a further system according to claim 20 is contained within the optical amplifier (4).

26. (PREVIOUSLY PRESENTED) An optical pump-/probe method by using a pulsed laser system according to one of claims 21 to 25, in which method

- an optical excitation pulse and a delayed optical monitoring pulse are directed onto a medium, wherein
- a signal induced by the delayed monitoring pulse is measured as a function of delay between the two pulses, wherein

- the pulse sequence of excitation and monitoring pulses and the delay from one to another is determined by the Pockels cell and the driver circuit of the Pockels cell.

27. (PREVIOUSLY PRESENTED) A material processing method by using a system according to anyone of claims 21 to 25, in which method

- a first laser pulse is directed onto the material surface such that a plasma is generated at the surface, and
- after some delay a number of further laser pulses is directed onto the plasma above the surface of the material, whereby
- the pulse sequence of the first laser pulse and the number of further laser pulses and the delay between these laser pulses is determined by the Pockels cell and the driver circuit of the Pockels cell.